Gate resistor installed
Dual N-channel MOSFET

KFCAB21520L
Data Sheet

For additional information or question, please contact Nuvoton Technology Corporation Japan.
www.nuvoton.co.jp

1. GENERAL DESCRIPTION

Gate resistor installed Dual N-channel MOSFET

For lithium-ion secondary battery protection circuits

2. FEATURES

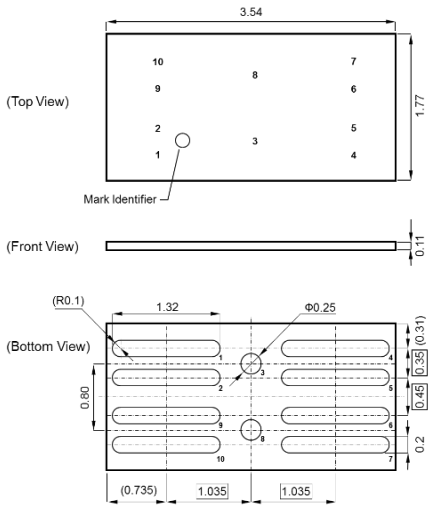
- Source-source ON resistance: RSS (on) typ. = 1.6 mΩ (VGS = 3.8 V)
- CSP (Chip Size Package)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1)

3. MARKING SYMBOL: 7T

4. PACKAGING

Embossed type (Thermo-compression sealing): 8,000 pcs / reel (standard)

Unit: mm

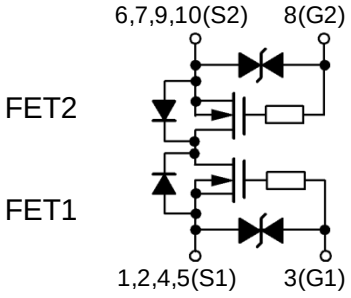


1,2,4,5. Source1(FET1) 3. Gate1 (FET1)

6,7,9,10. Source2(FET2) 8. Gate2 (FET2)

Package	TCSP1835011-N4
JEITA	—
Code	—

Equivalent Circuit



5. ABSOLUTE MAXIMUM RATINGS Ta = 25 °C

Parameter		Symbol	Rating	Unit
Source-source Voltage		VSS	12	V
Gate-source Voltage		VGS	±8	V
Source Current	DC *1	IS1	16	A
	DC *2	IS2	35	A
	Pulse *3	ISp	160	A
Total Power Dissipation	DC *1	PD1	0.54	W
	DC *2	PD2	3.8	W
Channel Temperature		Tch	150	°C
Storage Temperature Range		Tstg	-55 to +150	°C

6. THERMAL CHARACTERISTICS Ta = 25 °C

Parameter	Symbol	Rating	Unit
Thermal Resistance (ch-a)	Rth1 *1	232	°C / W
	Rth2 *2	33	°C / W

Note *1 Mounted on FR4 board (25.4 mm x 25.4 mm x t1.0 mm) using the minimum recommended pad size (36 μm Copper).

*2 Mounted on Ceramic substrate (70 mm x 70 mm x t1.0 mm).

*3 t = 10 μs, Duty Cycle ≤ 1 %

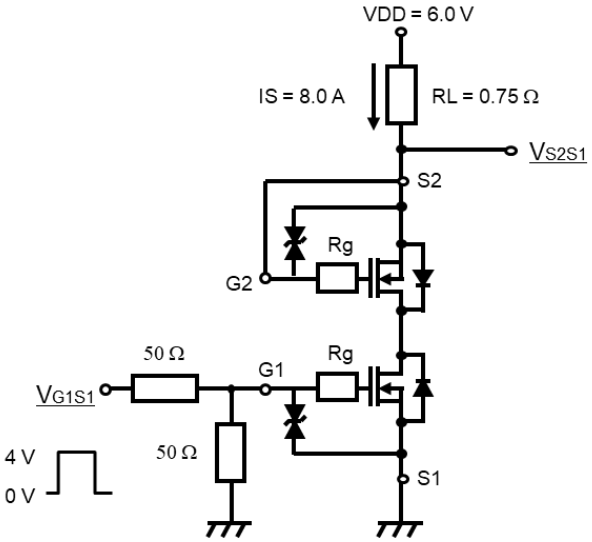
7. ELECTRICAL CHARACTERISTICS Ta = 25 °C ± 3 °C

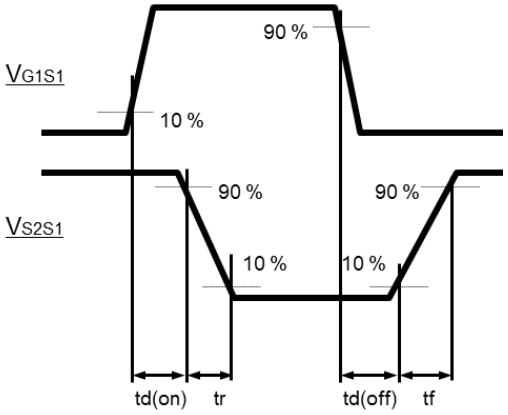
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Source-source Breakdown Voltage	VSSS	IS = 1 mA, VGS = 0 V	12			V
Zero Gate Voltage Source Current	ISSS	VSS = 12 V, VGS = 0 V			1.0	μA
Gate-source Leakage Current	IGSS	VGS = ±8 V, VSS = 0 V			±10	μA
		VGS = ±5 V, VSS = 0 V			±1.0	
Gate-source Threshold Voltage	Vth	IS = 1.64 mA, VSS = 10 V	0.35	0.90	1.40	V
Source-source On-state Resistance	RSS(on)1	IS = 8.0 A, VGS = 4.5 V	1.10	1.45	2.00	mΩ
	RSS(on)2	IS = 8.0 A, VGS = 3.8 V	1.15	1.60	2.10	
	RSS(on)3	IS = 8.0 A, VGS = 3.1 V	1.20	1.80	3.00	
	RSS(on)4	IS = 8.0 A, VGS = 2.5 V	1.40	2.30	4.50	
Body Diode Forward Voltage	VF(s-s)	IF = 8.0 A, VGS = 0 V		0.7	1.2	V
Input Capacitance *1	Ciss	VSS = 10 V, VGS = 0 V, f = 1 kHz		5250		pF
Output Capacitance *1	Coss			700		
Reverse Transfer Capacitance *1	Crss			630		
Turn-on Delay Time *1,*2	td(on)	VDD = 6.0 V, VGS = 0 to 4 V		1.5		μs
Rise Time *1,*2	tr	IS = 8.0 A		2.6		
Turn-off Delay Time *1,*2	td(off)	VDD = 6.0 V, VGS = 4 to 0 V		6.8		μs
Fall Time *1,*2	tf	IS = 8.0 A		4.1		
Total Gate Charge *1	Qg	VDD = 6.0 V		38		nC
Gate-source Charge *1	Qgs	VGS = 0 to 4 V		20		
Gate-drain Charge *1	Qgd	IS = 8.0 A		10		

Note Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

*1 Guaranteed by design, not subject to production testing

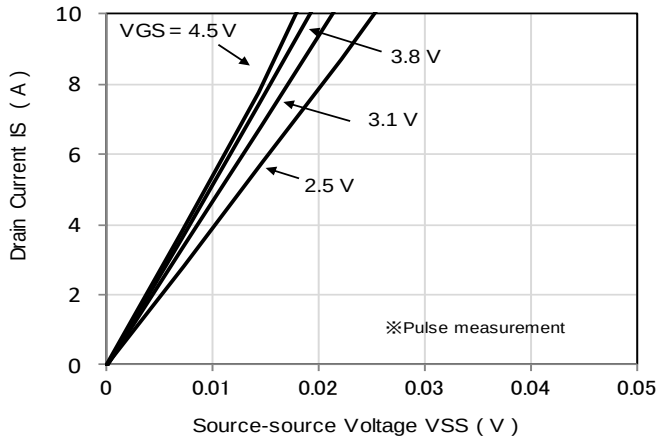
*2 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time



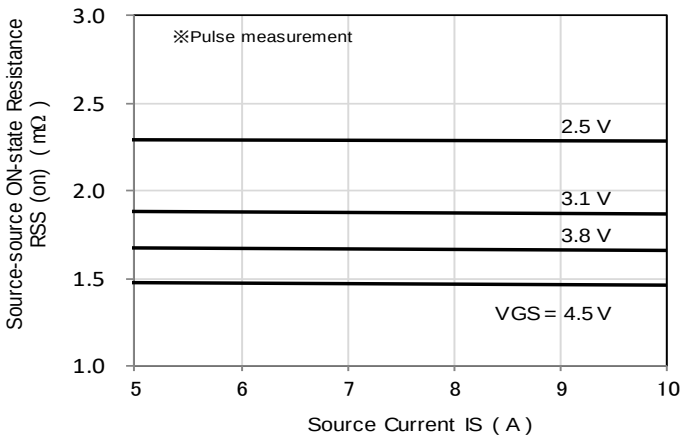


8. TECHNICAL DATA (Reference)

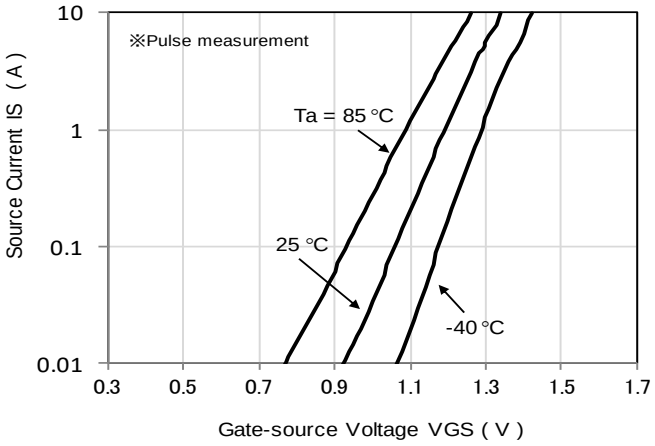
IS - VSS



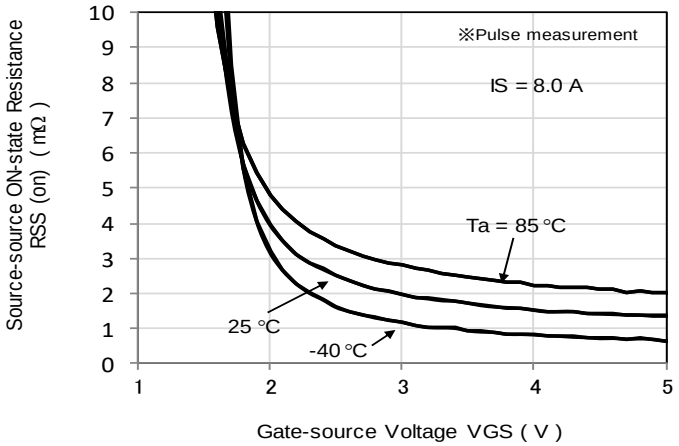
RSS(on) - IS



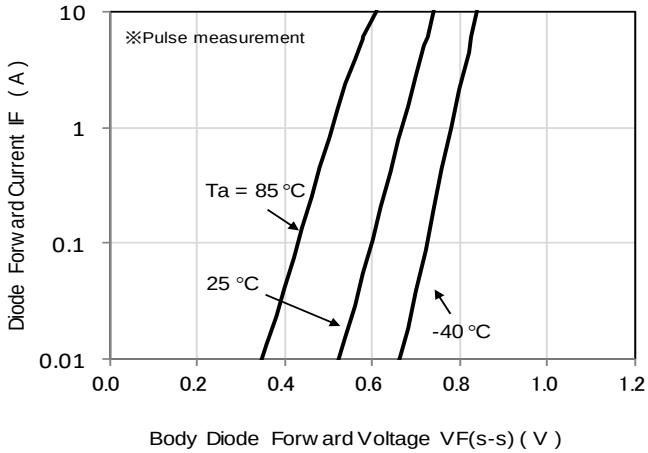
IS - VGS



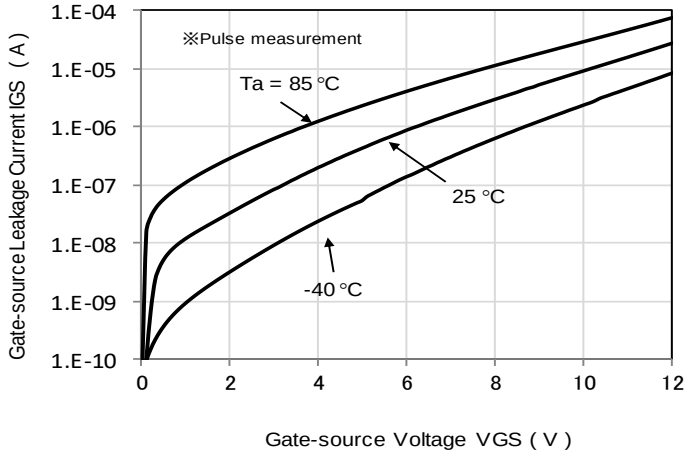
RSS(on) - VGS



IF - VF(s-s)

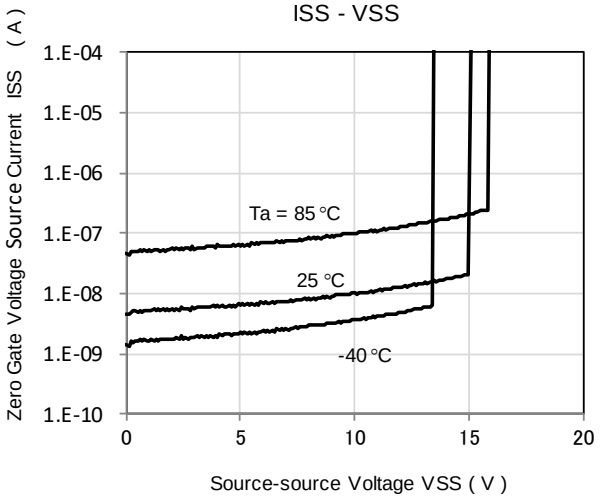


IGS - VGS

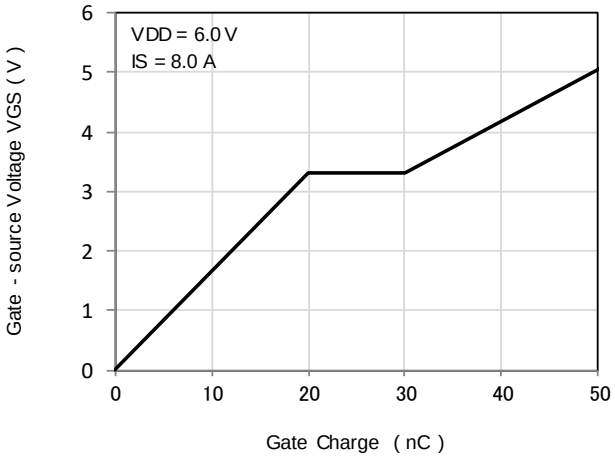


TECHNICAL DATA (Reference)

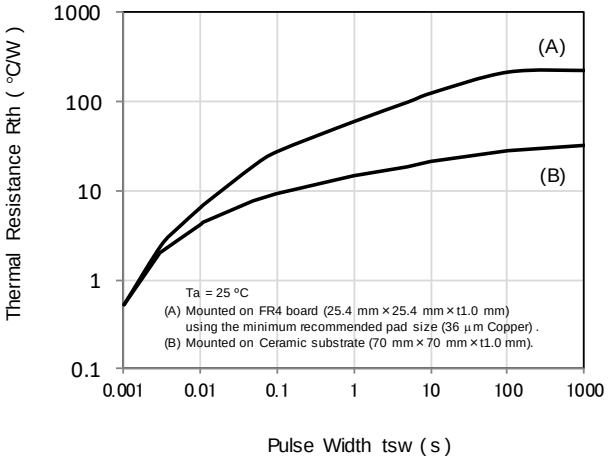
ISS - VSS



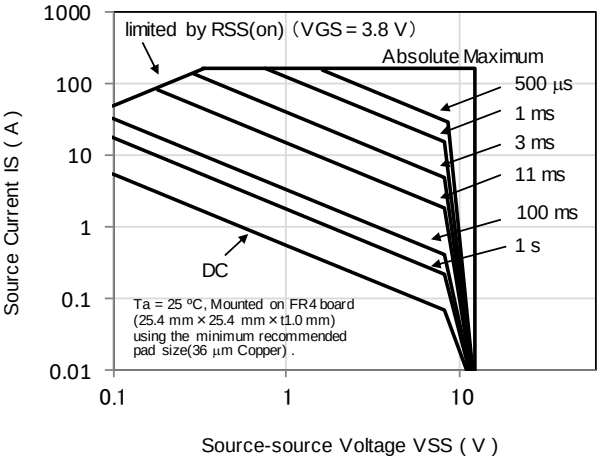
Dynamic Input/Output Characteristics



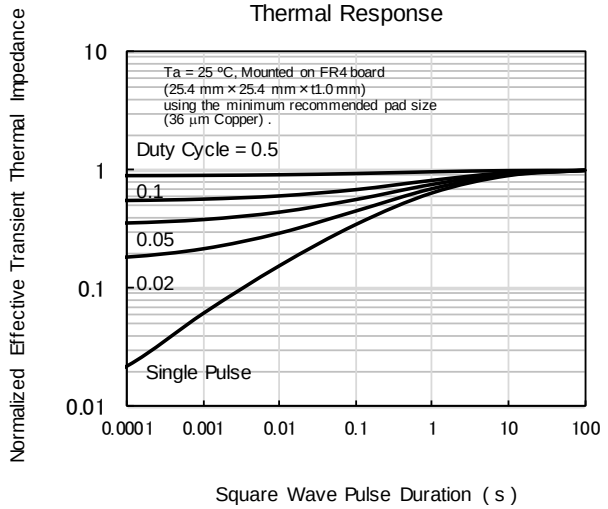
Rth - tsw



Safe Operating Area

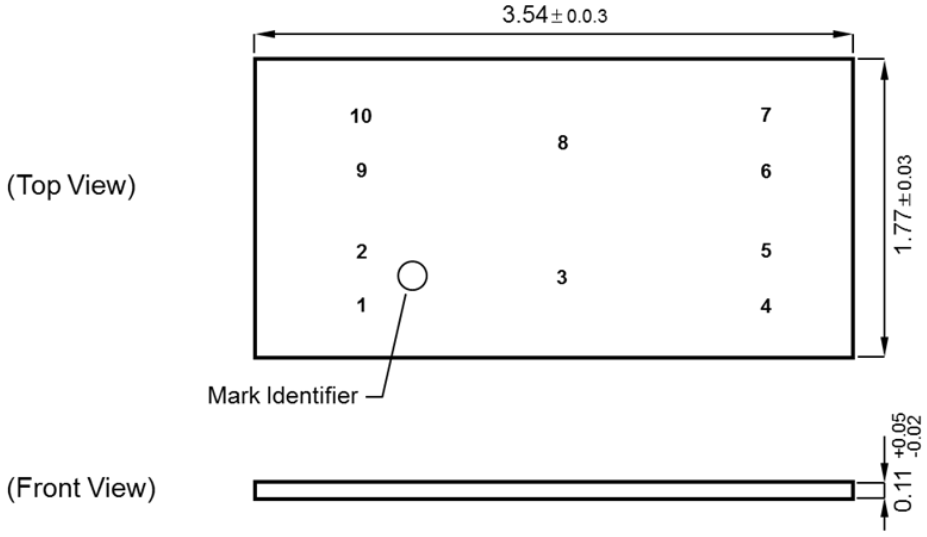


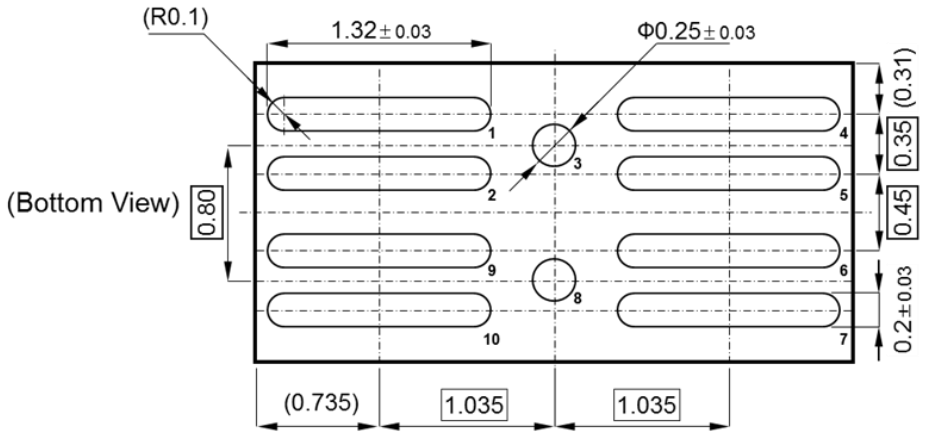
Thermal Response



9. OUTLINE

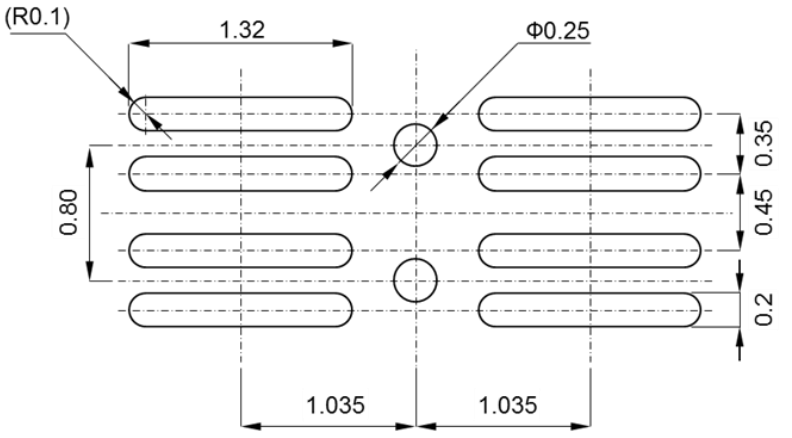
Unit : mm





10. LAND PATTERN (Reference)

Unit : mm



11. REVISION HISTORY

Date	Revision	Description
2021.2.3	1.00	1. initially issued.

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